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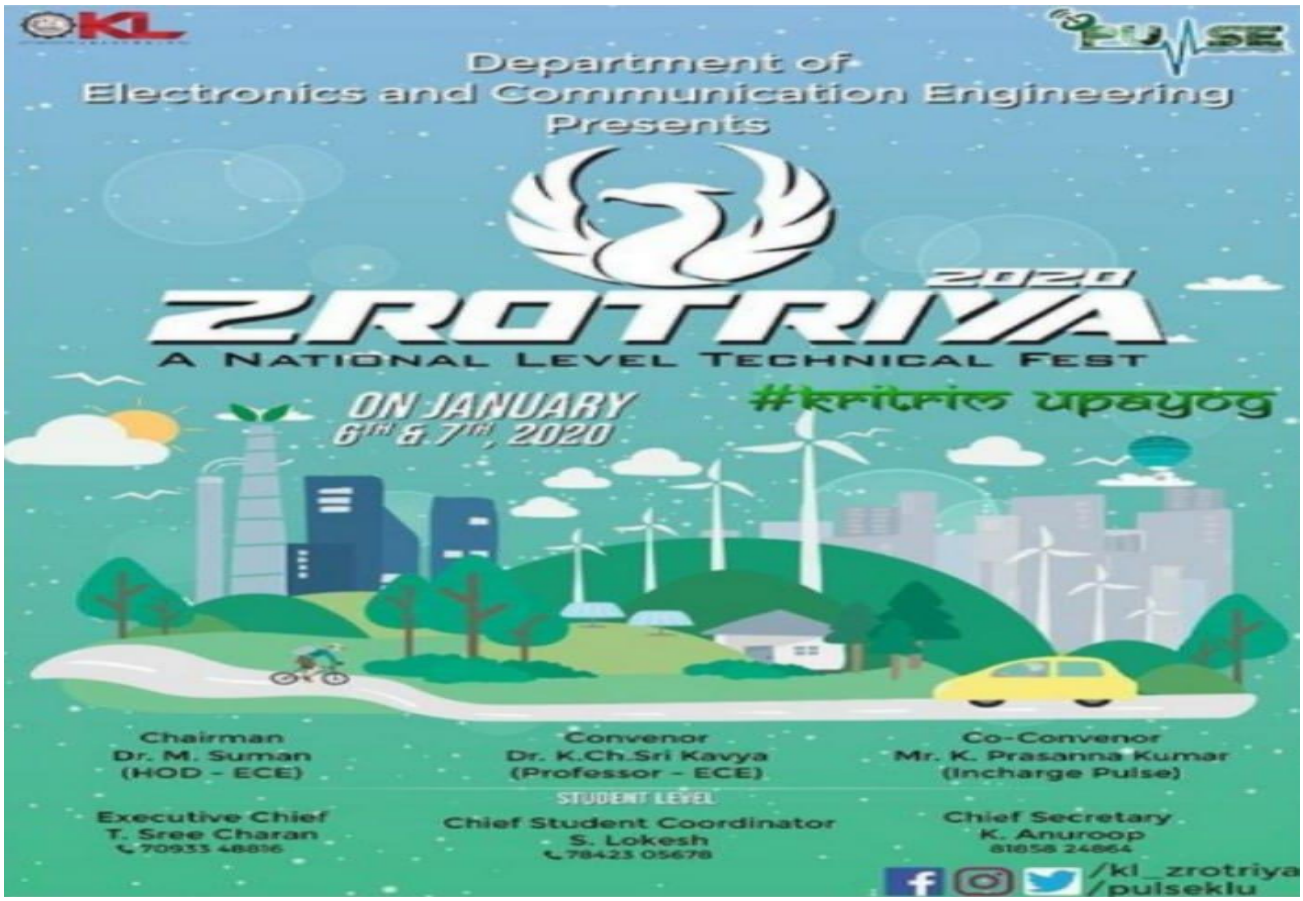

A Report on Zrotriya -20
Department of ECE Technical Fest

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Date of the Event : 06-01-2020

Name of the Event : BUGSPOT

Venue : R204(Lab)

No. of Participants : Students: 46, Staff: 04

A Report on BUGSPOT

on 06-01-2020

Objective:

The objective of the BUGSPOT event centered around fostering innovation and problem-solving within the realm of system-on-chip (SoC) technology. Participants were tasked with identifying, diagnosing, and resolving bugs or issues within complex SoC designs. Through this challenge, the event aimed to cultivate participants' analytical skills, technical proficiency, and teamwork abilities while promoting creativity and ingenuity in addressing real-world SoC challenges.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted "BUGSPOT" program on 20-03-2021 at R&D 107(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of Mr. I. Veera Raghava Rao, Ms. C.Priyanka, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

Organizer: Pulse - ECE Department Student Body

Introduction:

Pulse, the student body of the Electronics and Communication Engineering (ECE) department, orchestrated an innovative event titled "BUGSPOT" during the Zrotriya National Level Technical Fest. This event was meticulously crafted with the objective of fostering innovation and problem-solving within the realm of System-on-Chip (SoC) technology.

Event Overview:

BUGSPOT aimed to challenge participants' analytical skills, technical proficiency, and teamwork abilities through the identification, diagnosis, and resolution of bugs or issues within complex SoC designs. By delving into real-world SoC challenges, the event sought to cultivate creativity and ingenuity among participants while providing a platform for collaborative problem-solving.

Key Highlights:

Challenge Format:

Participants were presented with intricate SoC designs containing hidden bugs or issues. Their task was to meticulously analyze the designs, identify the anomalies, and devise effective solutions to rectify the detected bugs. The challenge format encouraged participants to employ a combination of theoretical

knowledge, practical expertise, and innovative thinking to overcome obstacles.

Team Collaboration:

The event emphasized the importance of teamwork, as participants worked collaboratively in teams to unravel the complexities of SoC designs. By fostering interdisciplinary collaboration and communication, BUGSPOT encouraged participants to leverage each other's strengths and perspectives in tackling challenging scenarios.

Technical Proficiency:

BUGSPOT provided participants with a platform to showcase their technical prowess in SoC design and debugging. Participants utilized state-of-the-art tools and methodologies to analyze and debug complex hardware and software interactions within SoC architectures. The event served as a testament to participants' proficiency in navigating the intricacies of modern electronic systems.

Innovation and Creativity:

Participants were encouraged to think outside the box and explore innovative solutions to address SoC bugs effectively. Through creative problem-solving approaches, participants demonstrated their ability to adapt to dynamic challenges and devise novel strategies for bug resolution. BUGSPOT served as a catalyst for cultivating a culture of innovation and ingenuity within the ECE community.

Conclusion:

BUGSPOT emerged as a captivating and intellectually stimulating event, showcasing the prowess of participants in tackling real-world SoC challenges. The event not only honed participants' analytical skills and technical proficiency but also fostered a spirit of teamwork, innovation, and creativity within the ECE community. As technology continues to advance, events like BUGSPOT play a crucial role in preparing future engineers to tackle the complexities of SoC design and contribute meaningfully to the field of electronics engineering.

Acknowledgments:

The success of BUGSPOT would not have been possible without the dedicated efforts of Pulse, the ECE department student body, as well as the enthusiastic participation of all attendees. Their collective commitment to excellence and passion for innovation contributed to making BUGSPOT a memorable and enriching experience for all involved.

Future Prospects:

Moving forward, Pulse is committed to organizing more such events that challenge participants to push the boundaries of innovation and problem-solving in electronic engineering. BUGSPOT has set a precedent for fostering collaboration, creativity, and technical excellence within the ECE community, and future editions of the event are poised to build upon this foundation and inspire the next generation of electronic engineers.

Outcome:

The outcomes of the BUGSPOT event culminated in the successful identification and resolution of bugs within various system-on-chip (SoC) designs, showcasing participants' adeptness in problem-solving and technical proficiency. Participants gained valuable hands-on experience in diagnosing and addressing complex SoC issues, furthering their skills and knowledge in this specialized field. Moreover, the event fostered collaboration and teamwork among participants, promoting the exchange of ideas and best practices. The successful resolution of bugs not only contributed to the advancement of SoC technology but also provided participants with a sense of accomplishment and validation of their capabilities within the field.

Event Photos:

Students involved actively in BUGSPOT Event

1	CHAMARTHI VENKATA SAI KARTHIK	180040457	10	Sai Karthik
2	Jonnalagadda Sree Harshitha	180040460	10	Harshitha
3	NIMMAGADDA. LAKSHMI SOUNDARIYA	180040470	10	N. Lakshmi
4	V.Mohana vamsi	180040474	10	vamsi
5	Amruthavani Bhumireddy	180040498	10	Bhumireddy
6	nagalla shiva	180040499	10	Shiva
7	DEVARASHETTY NIKITH	180040513	10	Nikith
8	K.S.V.Shanmukha Priya	180040542	10	[illegible]
9	Sk lubna kowsar	180040556	10	Kowsar
10	yeddula.vamsidhar reddy	180040559	10	vamsidhar
11	K.S.V.R.Kumar	180040570	10	Kumar
12	T. Bala Vamsi	180040575	10	vamsi
13	SIDDINENI POOJA NAIDU	180040576	10	S. Pooja
14	D VEERAJANARDHANA ACHARI	180040577	10	Achari
15	pisini pradeep	180040583	10	pradeep
16	Pokala satyanarayana	180040587	10	Satyanarayana
17	MALLEPULA DURGA DHEERAJ	180040595	10	Dheeraj
18	ANNEM AKASH	180040596	10	[illegible]
19	kolli nagasree	180040600	10	Nagasree
20	Vasireddy Balasaraswathi	180040604	10	Bala
21	Peram Hanvitha	180040608	10	Hanvitha
22	NAGA TEJA	180040613	10	Naga Teja.
23	lakshmi priyanka. palapati	180040616	10	P. Lakshmi
24	DammalapatiSriPrathyusha	180040619	10	Prathusha
25	Rohit Bonigala	180040633	10	Rohit
26	Chandana Dudam	180040644	10	Dudam
27	Chintapoodi pavankalyan	180040645	10	Pavankalyan
28	P.sailesh chowdary	180040658	10	[illegible]
29	G.vaishnavi	180040662	10	Vaishnavi
30	P SAI CHARAN	180040666	10	Charan
31	Dinesh Vardhan	180040667	10	Dinesh
32	V V N A Vishal	180040668	10	Vishal
33	KARISHMA BEGUM	180040669	10	K. Begum
34	Shaik Sameera Farheen	180040672	10	Farheen
35	Solasu charan	180040683	10	Charan
36	BELLAM VARUN KUMAR	180040689	10	Varun
37	Naveen Kalidindi	180040695	10	Kalidindi
38	KURUGUNTLA TANUJA	180040698	10	[illegible]
39	DAGGUPATI HARIPRASAD CHOWDARY	180040707	10	Hariprasad
40	T.Fakrunnisa Begum	180040708	10	Fakrunnisa.
41	peddiboinasrilaxmi	180040710	10	Srilakshmi
42	I.Narendra Datta	180040736	10	Narendra
43	Venne Ramya	190040564	10	V. Ramya
44	A.Chandra Haneesh	170040044	10	Haneesh
45	A.Sai Kiran	170040047	10	Kiran
46	A.Durga Sahithi	170040054	10	Sahithi

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Date of the Event **:** 06-01-2020

Name of the Event **:** CIRCUITRIX 1.0

Venue **:** R104(Lab)

No. of Participants **:** Students: 51, Staff: 04

A Report on CIRCUITRIX 1.0

on 06-01-2020

Objective:

The objective of CIRCUITRIX 2.0 was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation)Department of ECE conducted "CIRCUITRIX 2.0" program on 20-03-2021 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of J. Lakshmi Prasanna, Mrs. K.V.Sowmya, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF NSS) also took part in the event and together made the event a grand success.

The outcomes of CIRCUITRIX 2.0 encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:

Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.

In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Event Structure:

Circuitrix 2.0 comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

Circuitrix 2.0 achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, Circuitrix 2.0 fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

Circuitrix 2.0, as part of **"Zrotriya"** played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like Circuitrix 2.0 are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.

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DANISIRI GOPALAKRISHNA	170180017	12	
NEEMAJAMES KABANYWANYI	170180018	12	
IDRISADAM ISMAIL	170180020	12	
MAHMUDIDRIS	170180021	12	
INGRID DISMAS ASSEY	170180023	12	
NESTORY MBILINYI	170180024	12	
GODWIN ANTELM ANDREW	170180025	12	
FABIOLA CLEMENT UMBU	170180026	12	
IRENE LAZARO NGWETIAMA	170180027	12	
Md ABDALLA MOHAMED ABDELWAHAB	170180029	12	
KHALIL ASIMABDALLA GABIR	170180031	12	
ASHISH KUMAR SHARMA	180180001	12	
D BINDU SAI LAKSHMI	180180002	12	
D PRUDHVI KESHAVA	180180003	12	
DEVOJEET SARKAR	180180004	12	
GADDE SRIVALLI	180180005	12	
K BINDU HASINI	180180006	12	
L TIPPESWAMY	180180007	12	
M CHANDU VARDHAN	180180008	12	
MIKKILINENI DHEDEEPYA	180180009	12	
P GAGANA SRI	180180010	12	
T CHAITANYA KUMAR	180180011	12	
V APARNA REDDY	180180012	12	
VELAGA INDU	180180013	12	
KOLLIPARA VAISHNAVI	180180014	12	
NAINAVARAPU.SUMAKSHARIKA	180180015	12	
Amansnehi	180180016	12	
AMBOJI PRAVEEN KUMAR	180180017	12	
KELVIN MURITHI MUTHENGI	180180018	12	
OMAR TAREQ HASEEB ALDABBAGH	180180019	12	
JOACHIM ANATOLY SULLE	180180020	12	
SALIM ABDALLAH SEIF HAMAD ALZAKWAN	180180021	12	
JACKLINE (JACQUELINE) JOHN KIDUMBA	180180022	12	
ADILI ZACHEO MSELUKA	180180023	12	
AHMED MOHAMED YOUSIF WADI	180180024	12	
EMMANUEL KWIZERA	180180025	12	
S MOHAMMED ABDALLAH JAMEELALLAH	180180026	12	
NCHIMUNYA HANJALIKA	180180027	12	
TRESOR KAYIRANGA	180180028	12	
ANWER ABDULHALIM MUSSA EDRESS	180180029	12	
MALIK YAHIA ABDULRASOOL HUSSEIN	180180030	12	
JOSEE CONSCIENCE UFITESE ARATETA	180180031	12	
CH CHAITANYA	180180032	12	
G BHANU PRAKASH	180180033	12	

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Date of the Event : 07-01-2020

Name of the Event : NI MyRioWORKSHOP

Venue : R-6TH FLOOR (Lab)(COE)

No. of Participants : Students: 56, Staff: 04

A Report on NI MYDAQ on 07-01-2020

Objective:

An NI MyRio workshop aims to equip participants with the skills to utilize National Instruments' MyRiodata acquisition system. The workshop likely provides a hands-on approach, familiarizing attendees with the MyRio hardware and software, enabling them to configure the system for different data acquisition tasks and effectively interpret the collected measurements.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation)Department of ECE conducted "NI MyRio WORKSHOP" program on 20-03-2021 at R&D 6th floor (COE)(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of Dr.P.Pardha saradhi, Mr.Namgiri Suresh, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

Outcome:

An NI MyDaq workshop offers a valuable learning experience for those interested in data acquisition. Through the workshop, you'll gain hands-on skills in using National Instruments' MyDaq system. This includes learning how to configure the hardware and software to effectively collect data from various sensors and instruments. You'll also delve into methods for analyzing the acquired measurements, allowing you to extract meaningful insights from the data. By the workshop's conclusion, you'll be well-equipped to utilize the MyDaq system for your own data acquisition projects.

Event Photos:

Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.

In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

- Event Coordinator : Dr.P.Pardha saradhi
- Technical Co Ordinator : Namgiri Suresh

The Zrotriya National Level Technical Fest celebrated the realm of electronics with its flagship event titled "Advancements in Electronics: A Glimpse into the Future." This event was meticulously organized by the Electronics Branch of the fest, aiming to provide participants with insights into cutting-edge developments in electronic technologies.
Event Overview: The event comprised various segments tailored to cater to the diverse interests and knowledge levels of participants. From keynote presentations by industry experts to hands-on workshops and competitive challenges, the event encapsulated the essence of contemporary electronic innovations.

1. Keynote Presentations: Renowned experts from academia and industry delivered keynote addresses, shedding light on emerging trends and future prospects in electronics. Topics ranged from quantum computing and nanotechnology to Internet of Things (IoT) and artificial intelligence (AI) applications in electronics

2. Workshops: Practical workshops were conducted to impart valuable skills and knowledge to participants. Topics included PCB design, embedded systems programming, robotic automation, and 3D printing in electronics manufacturing. These workshops provided attendees with the opportunity to delve into hands-on learning experiences unde

the guidance of experienced instructors.

3. Technical Competitions: The event hosted a series of technical competitions designed to challenge participants' ingenuity and problem-solving abilities. Contests such as circuit design challenges, robotics competitions, and hackathons tested participants' creativity and technical prowess. Winners were rewarded with accolades and prizes, motivating participants to showcase their best efforts.

4. Exhibition: An exhibition showcasing the latest electronic gadgets, prototypes, and research projects was a major attraction of the event. Participants had the opportunity to interact with exhibitors, explore innovative technologies, and gain insights into real-world applications of electronics.

5. Panel Discussions: Engaging panel discussions were organized on pertinent topics such as sustainable electronics, ethical considerations in technology development, and the future of consumer electronics. These discussions fostered intellectual discourse and encouraged participants to critically analyze the societal impact of electronic advancements.

Conclusion: "Advancements in Electronics: A Glimpse into the Future" emerged as a resounding success, captivating the imagination of participants and fostering a spirit of innovation and collaboration. The event not only showcased the latest developments in electronics but also provided a platform for networking, learning, and inspiration.

As technology continues to evolve at a rapid pace, events like these play a pivotal role in shaping the future of electronic engineering and fostering a vibrant ecosystem of technological innovation. Acknowledgments: The success of this event would not have been possible without the unwavering support of the organizing committee, volunteers, sponsors, speakers, and participants.

Their collective efforts and enthusiasm contributed to making this event a memorable and enriching experience for all involved. Future Prospects: As the field of electronics continues to evolve, the Zrotriya National Level Technical Fest is committed to organizing more such events to keep pace with the dynamic nature of technology and provide a platform for

fostering creativity, collaboration, and excellence in the field of electronics engineering.

Event Photos :

Participants list:

Singaraju Siva Surya Kiran	180040092	ECE	Surya Kiran
Mutta Venkata Sai Deepak	180040094	ECE	Deepak
Nadimpalli Sai Kiran	180040096	ECE	N. Sai Kiran
Akula said Bala Siva jyothika	180040099	ECE	
panchayutula sonali	180040104	ECE	sonali
A.Narendra Reddy	180040106	ECE	
Gunnam Purna Chandrika	180040108	ECE	
Venkat Namana	180040111	ECE	V. Naveen
Dadi Sri Vandhana	180040114	ECE	vandhana
TANNEERU SAI BHARGAV	180040146	ECE	Sai Bhargav
M.Namratha	180040148	ECE	Namratha
N.Laya Sree	180040156	ECE	Laya Sree
Hemchand Pidikiti	180040157	ECE	P Hemachand
Dalali Arif	180040165	ECE	
Gembali Durga Narasimha Rahul	180040172	ECE	Rahul
Stalin Raj Kusuma	180040173	ECE	
shaik mohammed junaid	180040176	ECE	
B.Ramcharan Teja	180040179	ECE	B Teja
K.Sravani Annapurna	180040189	ECE	Annapurna
Galla.yamini Lakshmi	180040190	ECE	Lakshmi
Vyshnavi	180040192	ECE	Vyshnavi
Bhavya Tejaswi Manepalli	180040196	ECE	Bhavya Tejaswi
PAILA ANIL SAI JASWANTH	180040198	ECE	P. Anil Sai Jaswanth
SANIKOMMU YOGENDHRAAREDDY	180040199	ECE	S Yogendhra
PATRI SAI SREEHITH	180040205	ECE	Sreehith
pasupuleti.srija	180040228	ECE	
GEDDADA SAI SANDEEP	180040229	ECE	
Tadikamalla V V R N Sri Harsha	180040231	ECE	T.V.N.H.S.C
Mudigonda Vamsi Jwala Ramalingeswar	180040239	ECE	Vamsi Nikhila
MANDADI NIKHILA	180040244	ECE	
Maddula Tejasri	180040249	ECE	Tejasri
ADITYA GOKUL REDDY BHIMAVARAPU	180040254	ECE	Aditya
K.Nithya	180040270	ECE	K. Nithya
neimali sri nithya	180040271	ECE	
ripathi.gowthami	180040273	ECE	gowthami
ai chaitanya.Nandina	180040275	ECE	
ahnavi katte	180040283	ECE	
odali sai sivani	180040290	ECE	K. Sai
.Jyothika	180040301	ECE	Jyothika
HAIK ISMAIL BASHA	180040312	ECE	Basha
.KRISHNAPAVAN	180040318	ECE	Krishnapavan
.harsha	180040320	ECE	Harsha
ODAPATI SAI KRISHNA	180040332	ECE	B. Sai Krishna

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Date of the Event : **06**-01-2020

Name of the Event : POSTER PRESENTATION

Venue : R104(Lab)

A Report on POSTERPRESENTATION

on 06-01-2020

Introduction:

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. The Poster Presentation event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of the Poster Presentation event was to foster creativity and innovation among participants while emphasizing the importance of visual communication in conveying technical concepts. By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

Description:

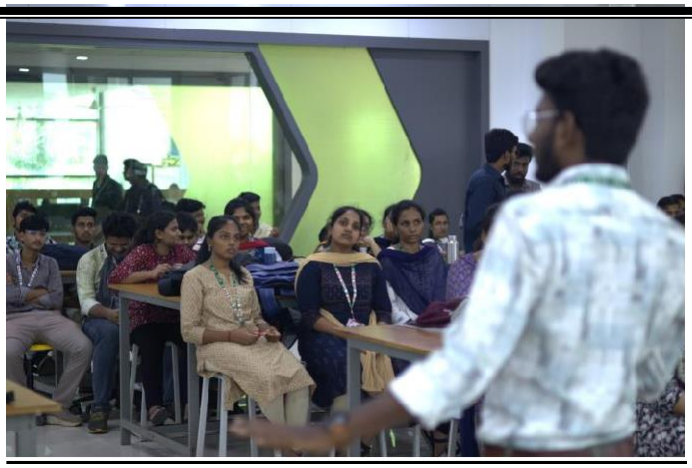
KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted "POSTER PRESENTATION" program on 20-03-2021 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of B Sai Sandeep,G L P Ashok, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of **"POSTER PRESENTATION"** encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



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In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Moment was captured in a single frame, encapsulating the essence of the

POSTER PRESENTATION event. In the photograph, a group of enthusiastic participants is seen engaged in an animated discussion, surrounded by stacks of papers and research materials. The intense expressions on their faces reflect the intellectual fervor and passion for knowledge exchange that permeated the event. With hands gesturing emphatically and minds abuzz with ideas, the participants epitomize the spirit of academic inquiry and collaboration fostered by POSTER PRESENTATION. This snapshot serves as a poignant reminder of the profound impact of scholarly endeavors and the transformative power of sharing knowledge within the dynamic milieu of Zrotriya.

JUDGES :

1) Dr K. Srinivasa Rao
2) Dr.Phani Kishore
3) Dr.Rehman
4) Dr.K.S.Ramesh

Event Structure:

POSTER PRESENTATION comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

POSTER PRESENTATION achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, POSTER PRESENTATION fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

POSTER PRESENTATION, as part of **"Zrotriya"** played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like POSTER PRESENTATION are essential for

Koneru Lakshmaiah Education Foundation
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Phone No. +91 8645 - 350 200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in
Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event **:** 07-01-2020

Name of the Event **:** PROJECT EXPO

Venue **:** R104(Lab)

No. of Participants **:** Students: 51, Staff: 04

A Report on PROJECT EXPO

on 07-01-2020

Introduction:

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. PROJECT EXPO event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of PROJECT EXPO was to offer participants an opportunity to present their innovative projects and research endeavors to a wider audience. The event aimed to encourage knowledge sharing, inspire creativity, and recognize outstanding achievements in various fields of technology. Additionally, PROJECT EXPO aimed to promote interdisciplinary collaboration and facilitate networking among participants, industry professionals, and academia.

Description:

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “PROJECT EXPO ” program on 22-03-2024 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of K.Sriathi Roy this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of **“PROJECT EXPO ”** encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

Event Photos:

Here we can observe by these two picture that our Honorable faculty members wereinvolved in this event and they came front to interview and conducted a HR round

In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.
Moment was captured in a single frame, encapsulating the essence of PROJECT EXPO event. In the photograph, a group of enthusiastic participants is seen engaged in an animated discussion, surrounded by stacks of papers and research materials.

Event Structure:

Project Expo featured an array of innovative projects spanning multiple disciplines, including but not limited to computer science, engineering, biotechnology, and sustainability. The event comprised the following components:

Project Exhibits:

Participants set up booths to showcase their projects, providing detailed demonstrations and explanations to visitors. The exhibits offered a hands-on experience, allowing attendees to interact with the projects and understand their functionalities.

PROJECT EXPO s:

In addition to physical exhibits, participants prepared posters summarizing their projects' objectives, methodologies, and outcomes. PROJECT EXPO s provided a concise overview of the projects and facilitated discussions among participants and visitors. participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Technical Talks and Workshops:

Project Expo featured technical talks and workshops conducted by industry experts and academic scholars. Topics covered a wide range of subjects, including emerging technologies, research methodologies, and career opportunities in various fields.

Judging and Awards Ceremony:

A panel of esteemed judges evaluated the projects based on criteria such as innovation, technical merit, practical applicability, and presentation quality. Winners were recognized and awarded prizes during the closing ceremony, acknowledging their contributions to the technological landscape.

Outcomes:

Project Expo at Zrotriya facilitated knowledge exchange, collaboration, and inspiration among participants and attendees. The event provided a platform for emerging talents to gain visibility, receive feedback, and forge valuable connections within the tech community. Moreover, Project Expo promoted innovation and encouraged participants to explore new frontiers in technology, addressing real-world challenges and driving positive change.

By showcasing a diverse range of projects, Project Expo highlighted the significance of interdisciplinary collaboration and the transformative power of technology in addressing global issues. Participants gained invaluable experience in project management, communication, and problem-solving, fostering their personal and professional development. The intense expressions on their faces reflect the intellectual fervor and passion for knowledge exchange that permeated the event. With hands gesturing emphatically and minds abuzz with ideas, the participants epitomize

Project Expo emerged as a cornerstone event within Zrotriya, encapsulating the essence of innovation, collaboration, and excellence in technology. The event not only celebrated the achievements of participants but also inspired future generations to pursue their passion for technology and make meaningful contributions to society. Moving forward, initiatives like Project Expo are instrumental in nurturing talent, fostering innovation, and shaping the future of technology on a global scale.

Moreover, PROJECT EXPO fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Zrotriya's PROJECT EXPO Event: A dynamic showcase of creativity and innovation, where participants craft visually stunning posters to communicate complex STEM concepts effectively.Empowering participants to unleash their design talents and express their ideas visually through PROJECT EXPO event at the national-level techno fest, Zrotriya

Conclusion:

PROJECT EXPO , as part of **"Zrotriya"** played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like PROJECT EXPO are essential for nurturing the next generation of electrical engineers and driving technological advancement in

Students List

Sl. No	REG NUMBER	NAME OF THE STUDENT	GUIDE	BATCH NO.	Title
1	160040342	KAKUMANI MANOHAR	Dr.Sai Krishna Santhosh	1	Burried powered 4T SRAM with improved right margin
2	160040527	MARASU SRIHARI			
3	160040557	MOHAMMAD BASHEER AHAMAD			
4	160041010	KOLLIPARA SAI SREE ROHINI	Dr K.Srinivasa Rao	2	Design and analysis of MEMEs based electro-spray thruster
5	160040976	YADAVALLI SUNDAR SAI KUMAR			
6	160040586	MUTHE SAHITYA			
7	160040656	PAKANATI JYOTHIRMAYEE	Dr.I.Govardhini	3	Image mosaic using sift and surf algorithm
8	160040996	KONAKALLA GEETHA SREE			
9	160040571	MUDRABOYINA MOUNIKA			
10	160040748	PUVVADA VENKATA SATYA SAI KUMAR	Dr.Ch.Sri Kavya	4	VLSI implementation of image cryptology with LFSR using reversable logic gates
11	160040409	KOGANTI KARTHIK			

12	160040754	RAMIREDDY PRANATH SIMHA REDDY			
13	160040883	THIRUMALASETTY BALAJI	Dr.D.Venkata Ratnam	5	Analysis of codegim during the solar maximum year 2019 in low lattitude region
14	160040920	VADLAMUDI VENKATA KRISHNA			
15	160040731	POTNURU SAI PAVAN			
16	160040153	CHEPURI DIVYA	Dr.K.Sarat Kumar	6	Reconfigurable image stegnography for data security applications
17	160040146	CHATTU NAGA RAJU			
18	160040351	KAMINENI NIHARIKA			
19	160040486	MADDALA CHINNI KRISHNA	Dr.M.Sridhar	7	Design of capacitance to digital CMOS based convertor for MEMS application
20	160040956	VENKATA SAI MANOJ POGADADANDA			
21	160040592	MYPATI ACHYUTH			
22	160040318	JANGAMREDDY CHANDANA REDDY	Dr.M.Sridhar	8	Detection of concentration of adulterants in gasoline using NI labview
23	160041037	SUNKARI V PRANITHA			
24	160041057	KAMISETTI YASASWINI SAHITHI			
25	160040109	BOLISETTY DHATHRI AISHWARYA	Susant kumar panigrah	9	Design of 3rd order signal delta modulator for bi-medical application
26	160040341	KAKULAPATI NAGA SAI AASRITHA			
27	160040491	MADDULA DILEEP VENKATA GOPI KRISHNA			
28	160040060	BANDARUPALLI VENKATESH	Dr.D.Venkata Ratnam	10	Estimation and analysis of DCB for navy system using air vat
29	160040290	GUNTAKA BHARATH KUMAR REDDY			
30	160040502	MAKINENI VARA SAI RAM			
31	160040983	YANAMANDRA LALITHA SRI VIDYA SAGARI	Dr.K.Kumar Naik	11	Design assymmetrical CPW fed patch antenna fo WBAN application
32	160040930	VANGA YAMINI			
33	160040967	VOOTLA SAI TEJA SREE			
34	160040413	KOLLU SAI RAJ	Dr.Md.Z.Rehman	12	Implementation of adaptive beem streering technique for phased array antennas
35	160040043	ARAVIND RAO K			
36	160040963	VISSAMSETTI VENKATA SAI AJAY			
37	160041017	VINJAMURI PAVAN KUMAR	Dr.Sai Krishna Santhosh	13	Design and analysis of GDI based full adder circu by using new XOR and XNOR
38	16004	CHENNAREDDY VENKATA			

	0150	SATHISH			
39	16004 0682	PASUPULETI SUMANTH			
40	16004 0752	RAGHUMANDA SANJAY NAIDU	Dr. S. Sunithmani	14	Modelling and analysis of bimorph piezo-electric energy harvester with tampered thickness
41	16004 0131	CHALLA HEMANTH KUMAR			
42	16004 0914	V SUDHEEP			
43	16004 0038	ARAMALLA SRI HARSHA	Dr.D.Sree Phani Kishore	15	Comparision of 9T SRAM and power gated 9T SRAM
44	16004 0724	POPURI AVINASH			
45	16004 0647	P KRISHNA SURENDRA			
46	16004 0726	POSINA REVANTH	Dr.M.Venkata Narayana	16	Color detection based vehicle movement by using iamge processing
47	16004 0795	SAMSANI SAI SIVAARCHITH			
48	16004 0966	VOLETI PAVAN KUMAR			
49	16004 0580	MUPPA SIVA SAI PALLAVI	Dr.Dr.P.Pardhasaradhi	17	Electromachanical line RF performance analysis of series configuration based MEMs switch
50	16004 0621	NASEEMA YASDHANI PATHAN			
51	16004 0324	JONNALA CHAITANYA LAKSHMI			
52	16004 0989	YEDDULA JITENDRA REDDY	Mr.B.John Philip	18	Validation of rain attenuation time series synthesizer
53	16004 0815	SHAIK HASHVATH VALI			
54	16004 0186	DEEVI PUNEET KUMAR			
55	16004 0671	PAPANABOINA SIREESHA	Dr.K.Ch.Sri Kavya	19	Humidity and moisture monitoring system for food logistics
56	16004 0475	M PUNITH			
57	16004 0406	KODIDALA SRI SAI VENKATESH			
58	16004 0935	VATTIGUNTA HYNDHAVI	Dr.K.Sarat Kumar	20	Rain fall prediction on MRR using mutiple linear regression
59	16004 0072	BATHULA VENKATESH			
60	16004 0899	TUMBETI SUNEEL KUMAR			
61	16004 1032	WULIGUNDAM RACHANA	Dr.I.Govardhini	21	Implementation of un conventional phased arrays using different methodogies
62	16004 1031	VENUM SAI CHARMILA			
63	16004 0215	DONTHIREDDY BHAVYASRI			
64	16004 0521	MANGU AVINASH	Mr.DV Sree Kumar Reddy	22	Design and performance analysis of meanders based RF MEMs shunt configuration switch

65	16004 0957	VENKATESWARLU RAYADURGAM			
66	16004 0529	MARLAPATI SRI HARSHA			
67	16004 0209	DODDAKULA PRIYANKA	Dr.D.Sree Phani Kishore	23	Energy efficient full adders for arithmatic applications using 130nm and 90nm
68	16004 0610	NANNAPANENI SHANMUKHA			
69	16004 0785	SADHU SAI ARTHIK			
70	16004 0148	CHEJETI MEGHANA	Dr.BTP Madhav	24	Circular slotted patch antenna using microstrip feed
71	16004 0177	DAMIREDDY ARUN KUMAR REDDY			
72	16004 0169	CHINTALAPUDI RAKSHITHA			
73	16004 0658	PALAPARTHI RAMA KRISHNA DHARMA TEJA	Dr.K.Hari Kishore	25	Area optimised run time reconfigurable ALU for digital systems
74	16004 0247	GANGAVARAPU KUSHWANTH			
75	16004 0110	BOLLA ROHITHA	Dr.T.V.Rama Krishna	26	Design and performance evaluation of slot annular ring patch antenna with various techniques for W-LAN applications
76	16004 0725	POPURI YASWANTH KUMAR			
77	16004 0317	JANGALA KEERTHANA			
78	16004 0353	KANAMARLAPUDI RAJASEKHAR	Dr.P.Lakshman	27	Design of 32-way wilkinson power divider using frequency
79	16004 0535	MATHI ROHITH BABU			
80	16004 0105	BOJJA AASHIKA			
81	16004 0049	ARUMALLA LAVANYA	Mr.Rahul Bosu	28	Performance evaluation of single ended voltage control ring oscillator for wireless
82	16004 0305	IGNATIUS JYOSTHNA LINGAREDDY			
83	16004 0134	CHALLA SAI PUNEETH			
84	16004 0364	KANDULA VEERA NAGA LAKSHMI	Dr.P.Lakshman	29	Cloud based temperature Monitoring system using IOT
85	16004 0487	MADDALI HIMA BINDU			
86	16004 0298	GUTTA NAVYA			
87	16004 0171	CHIRUMAMILLA ANUHYA	Dr.K.S.Ramesh	30	Area optimized run time reconfigurable ALU for digital systems using Mentor graphics
88	16004 0750	R V S D SAI PAVAN AVISETTY			
89	16004 0187	DEGALA KAVYA VINEELA			
90	16004 0210	DODDIPATI KALI NAGA MALLIKA	Mrs. K.Girija Sravani	31	Intigration of RF MEMS shunt capacitive switch with microstrip patch antenna
91	16004	GOTIKE MEGHANA			

	0278				
92	16004 0913	UYYALA NAGA BHAVANI DIVYA			
93	16004 0477	MACHARLA DEVISRI	Aswin kumar s v	32	Cloud server based vision controlled automated robotic vehicle
94	16004 0898	TULASI PARVATANENI			
95	16004 0463	KURUVELLA NAGA ARUN SAI KRISHNA			
96	16004 0056	B N V V SAI SPANDAN KRISHNA	Dr.K.Kumar Naik	33	Design a dual band circular polarized fractal patch antenna with fractal etched on the ground for wire less applications
97	16004 1024	JAYANTHI SAI VYSHNAVI			
98	16004 0723	PONNEKANTI SRAVANI			
99	16004 0163	CHILLAPALLI HARITHA	Mr. E. Raghuveer	34	A parametric DFT scheme for RAMs
100	16004 0284	GUMPENA VEDASRI LEELA			
101	16004 0094	BHOGADI ANIL KUMAR			
102	16004 0200	DHARMAVARAPU TRI VIKRAM	Dr. S. Sunithmani	35	Performance analysis of micro- needle array
103	16004 0188	DESAM GURUPRASAD REDDY			
104	16004 0265	GOLLA TARUN KUMAR REDDY			
105	16004 0093	BHOGADI VENKATA NIKHIL	Md.Z.Rehman	36	Complete home security system using IOT and NFC
106	16004 0331	K SUJAN KUMAR			
107	16004 0401	KINTALI SARAN KUMAR			
108	16004 0098	BITRAGUNTA NAGAVARDHAN	Mr. K. V K V L Pavan Kumar	37	Low noise high performance comparators
109	16004 0172	CHIRUMAMILLA MURALI KRISHNA			
110	16004 0619	NARU SIVA SAI NANDINI			
111	16004 0563	MOHITHA REDDY	Dr.K.S.Ramesh	38	Design and simulation of COMB drive MIM capacitive pressure sensor for harsh environmen
112	16004 0950	VEMPATI BALA TEJA			
113	16004 0259	GHANTA VENKATA SANTOSH KRISHNA TEJA			
114	16004 0042	ARAVETI PAVANI	Dr.P.Pardhasaradhi	39	Reconfigurable CSRR loaded MIMO antenna using PIN diode
115	16004 0301	GUVVALA RISHI			
116	16004 0293	GURRALA CHANDRA KIRAN			
117	16004 0553	MODADUGU S V S MANIKANTA KUMAR	Dr.M.Venkata Narayana	40	Design of digital controlled oscillator

118	160040320	JASTI VINEELA			
119	160040745	PUTTA SAI KIRAN			
120	160040091	BHIMAVARAPU NIHARIKA	Dr.M.Siva Ganga Prasad	41	Electro-machanical and RF investigations of fixed-fixed configuration based RF MEMS switch
121	160040831	SIDDAM MANOGNA			
122	160040070	BATHULA VENKAT			
123	160040234	GADDAM JAWAHAR REDDY	Dr. Shruthi Suman	42	14 transisteor double gate MOSFET full adder
124	160040816	SHAIK JAVED AHMED			
125	160040640	NOOLI PAVAN KUMAR			
126	160040393	KATTA CHANDANA	Dr. TV Rama Krishna	43	Design and performance evaluation of concentri annular ring patch antenna for WLAN and WIMAX applicaiotns
127	160040444	KOTTURU SURYA MANIDEEP			
128	160040189	DESU LOKESH			
129	160040039	ARANI SREEKAR PRANEETH	Dr.Madhukar Deshmukh	44	Energy detector for spectrum sensing in TVWS based cognitive radio networks
130	160040851	SUGGULA DEVI NAGA VENKATA SRI SAHITHI			
131	160040919	VADLAMUDI GRACY SAI			
132	160040158	CHERUVU KRISHNA CHAITANYA	Dr.N.Siddaiah	45	MEMS gas-sensor array for monitoring the percieved car-cabin air quality
133	160040030	ANIMALLA DURGA SINDHU			
134	160040307	INAVOLU VENKATA SAI SIVA KRISHNA KIRAN			
135	160040346	KALE DIMBAKESWARA MOOLAVIKASH PANI	Dr Fazal Noor Basha	46	FPGA based accident detection and monitoring system for safety traffic
136	160040359	KANDREGULA SEETHARAM			
137	160040858	SURARAPU NARENDRA			
138	160040084	BEZAWADA NAGA MANO SAI NAVEEN	Mr.LSP Sairam Nadipalli	47	IOT based E-health data acquisition system
139	160040161	CHILAMKURI GANESH KUMAR			
140	160040439	KOTAPATI VAISHNAVI	Mr.P.Kanakaraja	48	Number plate detection using image processing on rasberry-pi
141	160040685	PATHAN IMRAN KHAN			
142	160040422	KONATHALA SAI PRADEEP			
143	160040536	MATTA UDAY VENKATA MAHESH	Mr G.Rakesh Chowdary	49	Design and performance evaluation of D-flip flop
144	16004	DEVANABANDA KOUSHIK			

4	0191	REDDY			
145	160040711	PILLUTLA TEJASWI			
146	160040377	KARREDLA KRANTHI MAHESH	Dr. China Satyanarayana	50	Spectral analysis of pre-monsoon rainfall over AP and Telangana using maximum entropy method
147	160040473	LOKAM RAM GOPAL			
148	160040107	BOLIGALA VIJAYA LAKSHMI	M. Lakshmana Kumar	51	3-bit multiplier using GDI technique
149	160040435	KOPPINEEDI SRI SAI SRAVANI LAKSHMIBHARGAVI			
150	160041007	KADAMANTI BHASKAR			
151	160040203	DIRISAM MERCY	Dr. China Satyanarayana	52	Prediction of heat waves over AP and telangana region using WRF method
152	160040396	KAVURI HARI SRINIVAS			
153	160040962	VISSAMSETTI KARTHEEK SAI			
154	160040190	DEVABHAKTHUNI CHANDRIKA SOWMINI	J. L. Prasanna	53	Periodicities of north east monsoon rainfall over rayalaseema using maximum entropy method
155	160040162	CHILKURI SHWETA			
156	160040181	DASARI MEENAKSHI			
157	160040046	ARETI LAKSHMI SIREESHA	Mr Syed Shameem	54	Design and analysis of MEMS module for blood cells seperations using dielectro phoresis
158	160040147	CHEEDELLA VENKATA SIVA ANANTHA AKHIL			
159	160040362	KANDULA AJAY KUMAR			
160	160040460	KURRA HARSHITHA	Dr.Vipul Agarwal	55	Optical message encryption and decryption using parity generator and checker utilizing XOR gate
161	160040344	KALAPALA MERCY ROMITHA			
162	160040870	TANNEERU HAREESH	Dr.Vipul Agarwal	56	Design of low power area efficient 2:4 and 4:16 mixed logic line decoder
163	160040245	GANDLA VENKATA KALYAN			
164	160040067	BANGARU SAI SRI HARSHA			
165	160040217	DUDEKULA VAZAD VARDHAN	Dr K.Srinivasa Rao	57	Capacitive pressure sensor for blood pressure applications
166	160040121	BORA GIRISH NAIDU			
167	160040926	VALLABHAJOSYULA SRI VENKATA SAI TEJA	Mr Syed Shameem	58	Design and analysis of efficient trap structure to detect carcinoma cell
168	160040841	SOMU VENKATASAINIKHIL			
169	160040524	MANNEM SIVA SAI KUMAR			
170	160040003	ADARI SAI SRAVANI	Mr.E.Kiran Kumar	59	Design and implementation of image cryptography

171	160040933	VARTHI KARTHIK			
172	160040548	MERIGA JASMINE SINDHURA REBEKAH			
173	160040421	KOMMU SAMBASIVARAO	Mr Syed Shameem	60	Design and analysis chip cooling using phase change material for integrated circuits
174	160040660	PALLAPOTHU RAGA SAI NIKHILA	Dr. Sampad Kumar Panda	61	Design and implementation of pseudo random sequence generator using LFSR and its applications using GDA technique
175	160040458	KURAPATI VISHNU TEJA			
176	160040675	PARCHURU SPANDANA	Dr. C. Santhosh	62	Low noise dynamic comparator
177	160040885	THOPURI CHANAKYA			
178	160040424	KONDA DESWANTH SRINIVASA REDDY			
179	160040064	BANDLA SOWJANYA	Dr.D.Sree Phani Kishore	63	Implementation of OFDM using FPGA board
180	160040115	BONAGIRI BALA RAJESH			
181	160040828	SHAIK SUFFRAN			
182	160040588	MUTTE MEGHANA	Mr.DV Sree Kumar Reddy	64	Design of high sensitivity of gate all around FET
183	160040589	MUTYALA DEVI SAI LATHA			
184	160040382	KASINENI UPENDRA			
185	160040749	PUVVADA YESWANTH	Dr G.R.K.Prasad	65	IOT intelligence and automation with thinger.io
186	160040705	PERAKA MONY PREETHAM			
187	160040894	THUMMA PRASANTH KUMAR REDDY			
188	160040850	SUDINEEDI VENKATA NAVYA SRI	Dr M.Siva Kumar	66	Reccursive approach of parallel self timed adder of 64 bit using verilog logic
189	160040255	GAVIRNI VENKATA HARISH			
190	160040248	GARAPATI NAGA KISHORE			
191	160040659	PALLAPOTHU RAGA SAI AKHILA	Dr. S. Arunmetha	67	Design of gas sensor for safer industries using IoNT
192	160040921	VAKALAPUDI SAI RAJESH			
193	160040288	G PENCHALA NARASIMHA SAI MAHESH			
194	160040991	YEMINENI SYAMALA	Ms.C.Prinyanka	68	Design of fast binary counter using QCA
195	160041016	VILUVALA BHANU TEJA			
196	160041001	AMMU SAMHITHA			
19	16004	MOVVA SAI NAVYA	Dr.Sai Krishna	69	Analysis of high speed low power dynamic

17	0569		Santhosh		comparator
19 8	16004 0910	UPPELA KOTI REDDY			
19 9	16004 0623	NAVALE YASWANTH KUMAR			
20 0	16004 0337	KAKARLA AVINASH	Dr. S. Sunithmani	70	Performance enhancement of capacitive pressure sensor with differetn geometries
20 1	16004 0949	VELPURI NAGA SAI			
20 2	16004 0655	PAKALAPATI MOHAN VINAY	Dr. Sampad Kumar Panda	71	Performance analysis of DET flip flop using C-elements
20 3	16004 0075	BATTULA SURESH GOPI CHAND			
20 4	16004 0185	DEEPIKA AKALAMKAM	Dr.N.Prabhakaran	72	Implementation of approximate multiplexer for efficient compressor design
20 5	160041 059	AMMISETTI VISHAL SAI PAVAN			
20 6	16004 0646	P ANIL KUMAR			
20 7	16004 0468	LATCHI BINDU	Dr.Vipul Agarwal	7	Design and simulation of high sensitivity RF MEMs switch
20 8	16004 0405	KODATI DIVYA SAI			
20 9	16004 0411	KOLAMURI RAMYA CHANDRIKA			
21 0	16004 0694	PATNANA RUPANJANI	Dr M.Siva Kumar	74	Implementation of Combinational Circuits Using CMOS and GDI Techniques
21 1	16004 0066	BANDLAMUDI ARAVIND			
21 2	16004 0515	MANDALA MUKESH REDDY			
21 3	16004 0876	TATTUKOLLA GOWTHAMI	Mr.Sanath Kumar T	75	Comparitive analysis of C-element and D-element dual edge triggered flip flop for low power VLSI apllications
21 4	16004 0979	YAKKALI HARSHITHA			
21 5	16004 0648	PACHIGOLLA N S V RENUKA SURYA AVIGHNA			
21 6	16004 0753	RALLAPALLI SAI VINEETH	Mr. Sk. Ahmadsaidulu	76	Performance measures of different gate oxide materials in gate all around FET
21 7	16004 0975	YACHAMANENI TANMAYEE			
21 8	16004 0123	BURADA MEENAKSHI			
21 9	16004 0558	MOHAMMAD JAMEELA SULTANA	Dr M.Siva Kumar	77	Dealy estimation of different approximate reverse carry propagate adder
22 0	16004 0315	JANALAPATI DHAMINI			
22 1	16004 0004	ADUSUMILLI SANJAY			
22 2	16004 0909	UPPALAPATI CHANDU SRINIVAS			
22 3	16004 0688	PATHAN SHANAWAZ	Dr.P.S.R.Kishore	78	A novel design of flip flop circuits using gate diffusion input (GDI)

224	160040494	MADHALA PRUDVI			
225	160040128	CHALASANI PEEYUSH			
226	160040707	PERECHARLA PRANEETH BABU	Biswajit Jena	79	A High-Speed or Low-Power Multiplier with Spurious Power Suppression Method
227	160040142	CHANDRA LAKSHMI HARIKA			
228	160040884	THOMMANDRU JAYARAM	Dr.S.Rooban	80	A low power differential dynamic comparator
229	160040218	DUGGINENI PRAVEENKRISHNA			
230	160040840	SOMIREDDY JAYA SAI SRI			
231	160040925	VALIVETI SIVA PRUDHVISH	K. Mariyapriyadarshini	80	Low power and less-delay fast full adder by exploring new XOR and XNOR gates
232	160040531	MARRIBOINA DIVYA SREE			
233	160040442	KOTTHAPALLI ALEKHYA			
234	160040587	MUTHIMSETTY GOPIMOHAN	Dr. R S Ernest Ravindran	82	Design, simulation and analysis of micro-mirrors on aero-space applications
235	160040332	K UDAY KUMAR REDDY			
236	160040296	GUTHA TEJASWANI NAIDU			
237	160040826	SHAIK SABEENA	Dr. R S Ernest Ravindran	83	Encription techniques using finite field multipliers for cryptography
238	160040028	ANDE THANUSHA SAI			
239	160040774	REDDYCHERLA SAIDA REDDY	Dr. K. Sarat Kumar	84	A monopole antenna with an array of C-shaped slots for dual band
240	160040367	KANTIPUDI GOWTAM CHOWDARY			
241	160040932	VARANASI SRI HARSHA VARDHAN			
242	160040195	DEVIREDDY RAM KIRAN	Dr.K.Ch.Sri Kavya	85	Facial emotion detection
243	160040381	KARUSALA VINAY KUMAR			
244	160040866	TALABOTHULA KALYAN			
245	160040954	VEMURI SRI SINDHU	Dr. I. Govardhini	86	Design and analysis of circular ring resonator for Ku band applications
246	160040781	S V S L SREEJASREE MUPPARAJU			
247	160040184	DAVULURI SAI KRISHNA			
248	160040414	KOLLURI NAVEEN	Dr. M. Venkat Narayana	87	Design of a non-foster elemental patch antenna for cellular applications
249	160040199	DHARMAVARAM ANIRUDH			
25	16004	MADDINENI SRIRAM			

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251	160041000	ALLAMSETTY KRISHNA VAMSI	Dr. M. Sridhar	88	Signal reconstruction using PCA
252	160040721	PONNAM REDDY HARSHA VARDHAN REDDY			
253	160040576	MUNAGALA SAILITEESH			
254	160041036	KATABATTUNI SUMA	Dr. S. Koteswara Rao	89	Tracking a maneuving target using UAV
255	160040490	MADDUKURI AJAY KUMAR			
256	160040579	MUNUKUTLA SAI CHARAN DAKSHINA MURTHY			
257	160040549	MERUVA RAMA KOTESWARA RAO	Dr. Sampad Kumar Panda	90	Design and implementation of smart college network by using cisco packet tracer
258	160040856	SUNKU VENKATA GANESH KUMAR			
259	160041003	BETALA SUPRIYA			
260	160040207	DODDA JAYA CHANDRA REDDY	Dr.Habibulla Khan	91	Dual element MIMO antenna with defective ground for satellite, fixed mobile, radio navigation and ISM band applications
261	160040417	KOMATLA RUDRA VENKATA NARASIMHA REDDY			
262	160040202	DHULIPUDI MAHIDHAR			
263	160040379	KARRI CHETAN REDDY	Dr. N. Prabhakaran	92	Detection of counter feit currency using DWT an SVM algorithm
264	160040846	SRIDHARA S S M PRANAV KUMAR			
265	160040182	DASARI ANANTHA SAI KUMAR	Dr. Deepak Kumar Nayak	93	An ultra wide band octogonal patch with reconfigurable narrow band antenna for cognitive radio apllications
266	160040253	GARLAPATI VENKATA KOWNDINYA			
267	160040387	KATEPALLI RAMESH	M. Venkata Sreenu	94	A novel method to detect OSA using deep convolution neural networks
268	160040479	MADALA MOUNEESH			
269	160040113	BOMMASANI SRI LAKSHMI HARIKA			
270	160040765	RAYAPROLU AKARSH LAKSHMI VINAY KUMAR	Dr.P.Satyanarayana	95	Artificial neural network model for estimation of suspended sediment load in krishna river basin, India
271	160040257	GEETHIKA U			
272	160040980	YALAMANDALA SIVA RANJANI			
273	160040427	KONDAPALLI PREETHI	Dr. S. Arunmetha	96	Implementaion and investigation of an optimal full adder design for low power and reduced delay conditions
274	160040609	NANDURU PAVANI NAGA SAI CHANDANA			
275	160040079	BEESA SAI TANUJA			
276	160040533	MASETTY BHARGAV	Dr. Shruti Suman	97	A low cost smart irrigation system using MQTT protocal

27 7	16004 1061	ANKIT DAS			
27 8	16004 0887	THOTA SAI SRIPRIYA			
27 9	16004 0144	CHAPPIDI SILAS	Dr. S. Rooban	98	Design of a signal tracking system using GPS and GSM with aurdino tool
28 0	16004 0626	NEELI VENKAIAH			
28 1	16004 0818	SHAIK KARISHMA			
28 2	16004 0824	SHAIK MUHAMMAD IMRAN	Dr.Fazal Noor Basha	99	Designing of SFQ Multiplier with 4-bit with Efficiently through High-Speed Hardware
28 3	16004 0024	AMBATI NAGA SANDHYA REDDY			
28 4	16004 0617	NARNE HARI PRIYA	Dr.R.Revathi	100	Investition of ionosphere variability due to geomagnetic indices and electron density using GPS TEC
28 5	16004 0495	MADICHARLA NAGA SATYA SIVA PRASAD			
28 6	16004 0906	ULLAM VENKATA KRISHNA	Mr.Selvakumar R	101	Simulation of autonomous vehicle parking using hirarcle optimization based collision avoidance algorithm
28 7	16004 0159	CHIDIPOTHU PAVANI PRIYA			
28 8	16004 0645	OBULASETTY PRAVALLIKA			
28 9	16004 0990	YELCHURU NARASIMHA DHEERAJ	Mr.D.Sreenivasa Rao	102	Elecctrical reconfigurable band-notch antenna for microwave sensing applications
29 0	16004 0126	CHAGANTI ROHIT REDDY			
29 1	16004 0082	BERA PRABODH SRIKAR SANDEEP			
29 2	16004 0584	MUPPINENI SAI NEELIMA	Mr.GLP Ashok	103	Implementation of enterprise network with end-to-end solution
29 3	16004 0026	AMMULA VAMSHI			
29 4	16004 0015	ALLA NIKHILA SRI	V.Sahiti	104	Identification of leukemia
29 5	16004 0446	KOYA KUNDANA GOWRI			
29 6	16004 0412	KOLLIPARA SAMPATH	Dr.P.Poorna Priya	105	Detection of objects for automous cars using LANE detection method
29 7	16004 0782	SABBISETTI CHARAN SRI SAI TEJA			
29 8	16004 0556	MOHAMMAD AKRAM HUSSAIN			
29 9	16004 0366	KANKARA LAVANYA	N.Durga Indira	106	Optimization based brain tumor segmentation
30 0	16004 0300	GUTTI DHAMAR SAI KIRAN			
30 1	16004 0204	DIRSIPAMU PUSHPA SASHANK			
30 2	16004 0773	REDDYBATHULA VIJAY KUMAR REDDY	N Bala Chakravarthy	107	Predection of diabetes using hyper parameter tuning
30	16004	SIGAMSETTI DORABABU			

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305	160040969	VUDATHA VEERA VENKATA DILEEP	Aswin kumar s v	108	Paddy leaf disease detection using SVM classifier
306	160040137	CHALLAPALLI UJWALA			
307	160040684	PATHAN BAHALUL KHAN			
308	160040481	MADALA TARUN SAI	R Challadurai	109	Pest detection using image processing techniques
309	160040934	VASSIREDDY PARIMALA			
310	160040794	SALAKA MUKESH KUMAR	M.Anil Kumar	110	Voice Based Hot Cold-Water Dispenser System using Ras Pi
311	160040613	NARAVA PRANAY MANIKANTA			
312	160040608	NANDINI NIDUMOLU	B.Murali Krishna	111	Optical character recognition
313	160040007	AKIRI HARSHA VARDHAN			
314	160040541	MEDEM YESVITHA DURGA			
315	160040736	PUJA PAMULAPATI	Dr. K. Kumar Nayak	112	Design of dual band patch antenna at KU band for wireless applications
316	160040523	MANNAVA PRIYANKA			
317	160040931	VANKAYALAPATI SUDHA SRI	Dr. D. V. Ratnam	113	Correlation between Inospheric TEC and the DCB stability of GAGAN recievers
318	160040706	PERAM SAI KEERTHI			
319	160040526	MARAMREDDY BHANU PRAKASH REDDY			
320	160040961	VISHNUMOLAKALA JYOTHIKA	Dr.Md.Z.Rehman	114	16 element rectangular patch array antenna for 5G applications
321	160040787	SAGIRI VAMSI KRISHNA			
322	160040500	MADU SAI POORNA CHANDRA SHEKAR			
323	160040117	BONTHU NEHA REDDY	Dr.Habibulla Khan	115	Design and analysis of a compact MIMO antenna with defective ground structure
324	160040271	GOPINA UMA MAHESWARI			
325	160041011	KOTARI RAMA PRATHYUSHA			
326	160040041	ARAVAPALLI SRAVANI	R.Sekar	116	A novel approach of MRI-CT image fusion using CWT for finding disease location
327	160040673	PAPINENI DIVYA			
328	160040825	SHAIK MUJEEB			
329	160040507	MALLISETTY GNANA ADITHYA	Mr.Namagiri Suresh	117	Android applications based on driver drowsiness detection

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331	160040256	GAYAM AKIL			
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334	160040326	JUBER AHAMED SK	Dr.Poorna Priya	119	House price predection using regression
335	160040669	PANTHANGI SIVA SWAPNIC GUPTHA			
336	160040852	SUKRUTHI	M.Venkateswara Rao	120	Robust transmission using turbo codes
337	160040306	IMMADI SRI HARSHITHA			
338	160040546	MEKALA MARUTHI SOWMYA			
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340	160040896	TIRUMALASETTI RAVALIKA			
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342	160040564	MONDEM VEERA BHAVANI SHANKAR	Dr. S Karthick	122	LPG lakage detection and prevention system using aurdino and nodeMCU
343	160040943	VEGI JETHENDRA			
344	160040476	M VENKATESH			
345	160040510	MANAMURI VASAVI LAKSHMI SAI PRASANNA	Dr. MZ Rehman	123	Spectrum sensing using energy detection for cognitive radios
346	160040650	PADAKANDLA HITHENDRA SAI KUMAR			
347	160040654	PAGOLU ANUPRIYA			
348	160040347	KALVALA NIKHIL SAI RADHESH	Dr. B.T.P Madhav	124	Analusis of CPW fed modified Z-shaped reconfigurable array antenna for automative communications
349	160040757	RANGARAJU VENKATA ABHIRAM			
350	160040329	JUNUTULA HARISH			
351	160040607	NAMBURI HARSHITH VARMA	Dr. M. S. G. Prasad	125	Volume based method for spectrum sensing
352	160040244	GALI YAMINI DEVI			
353	160040776	REKADI RENUKA SAI MADHURI			
354	160040878	TEKI SAI VEERA SAKETH	Dr. Madhukar Deshmukh	126	Design and analysis of split ring resonator using patch antenna
355	160040691	PATIBANDLA SAMBA SIVA RAO			
35	16004	D S K S V L S N S PREMA SRI	Dr.Y.Usha Devi	127	Layline detection for automation cars

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35 7	16004 0445	KOVI VAMSI KRISHNA			
35 8	16004 0632	NIDADAVOLU RAVITEJA			
35 9	16004 1053	MANDADI KARTHIK	Dr.M.Suman	128	Prediction of type of cough from cough sound
36 0	16004 0311	JAKKA SURYA MANI KUMAR			
36 1	16004 0432	KONIDENA GUNAPRIYA			
36 2	16004 0493	MADETIMUNI BHUPESH	B.Suresh	129	IOT Based Heart Monitoring System Using ECG
36 3	16004 0343	KALANADHABHATTA SREE POOJA			
36 4	16004 0240	GADE VIJAYA VENKATESWARA REDDY	Mr.M.Ravi Kumar	130	Image enhancement for ultra sound image using sobel edge detection
36 5	16004 0238	GADE KALYAN KUMAR			
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36 7	16004 0743	PUSHADAPU BHARGAV RAM	Dr. D.Bhavana	131	Diabetes detection using thermal image using adaptive filter and mean filter
36 8	16004 0312	JAMMI GUMPULA SAI BHARGAV			
36 9	16004 0676	PARISILLA SRINIVAS			
37 0	16004 0404	KODALI RAMA KRISHNA	Dr. Mannepalli Kasiprasad	132	Speech forgery detection of framed sentences from syllables using DTW
37 1	16004 0703	PELLURI VAMSI KRISHNA			
37 2	16004 0454	KUNDURU NARSIMHA RAO	Dr.S.Koteswara Rao	133	Under water target tracking system using active sonobuoys
37 3	16004 0423	KONDA BHARGAV SRI SAI			
37 4	16004 0902	TUMMIDI SURYA NAGA DHEERAJ	Dr.A.S.C.S.Sastry	134	Sentimental analysis based on text and emoticons
37 5	16004 0352	KANAKAM VEERA VENKATA ANUDEEP			
37 6	16004 0945	VELAGAPUDI VENKATA NAGA SAI GNANA SATWIK	Dr.G.V.Subba Rao	135	Subsurface anamoly using artificial neural networks
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37 8	16004 0229	EMANI SOWJANYA	Dr.M.Venugopal Rao	136	Automated clss room attendance system using PCA
37 9	16004 0441	KOTARI SAI KIRAN			
38 0	16004 0174	CHITICASI GANESH			
38 1	16004 0993	YERNAGULA VIJAY KUMAR	Dr. G Naveen Kishore	137	Sound source localization using 3D microphone array in flight mode
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386	160040505	MALLAVELLI JAYA SRINANDAN	Dr.Ch.Raghava Prasad	139	Real time attendance system using CNN
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389	160040624	NAVYA SRAVANI JAMMALAMADAKA	Dr.M.SUMAN	140	Comparitive analysis of big five personality prediction using machine learning techniques
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391	160041052	BASE SHANKAAR	Dr. Vinay Kumar Mittal	141	Analysis of ECG signals for the detection os sleep apnea
392	160040972	WUYYURU SAMYUKTHA			
393	160040045	ARETHOTI RATNA PRAKASH			
394	160040216	DUDALA TARANI	Dr.MVD Prasad	142	Detection and segmentation of rocks on lunar surface using deep learning
395	160040196	DEVIREDDY VENKATESH			
396	160040263	GOLI HEMA SUNDAR			
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399	160040578	MUNNANGI GOVARDHAN REDDY			
400	160040537	MATTIPALLI MANIKANTA PRASANTH	Dr.B.Polaiah	144	Age and gender estimation using computer visio
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402	160040416	KOMARAVOLU SAIRAMAKRISHNA YASWANTH			
403	160040467	LANKIREDDY RAJANI	Dr. Vinay Kumar Mittal	145	Glaucoma detection using SVM classifier
404	160040249	GARAPATI YASHASWINI SREE NEHA			
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414	160040095	BHRUGUBANDA YASWANTH SAI			
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416	160040408	KOGANTI ACHARYA SAI KRISHNA BHARGAV	Dr.A.S.C.S.Sastry	150	Blob detection based vehicle counter
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49 2	16004 0534	MATHI PRABHU KISHORE			
49 3	16004 0544	MEKA TEJESH REDDY			
49 4	16004 0108	BOLISETTI RAMA CHANDRA	Dr.R.Revathi	179	A Simulation Model of Networked Tracking for Anti-Submarine Warfare
49 5	16004 0597	NAGA SAI SATYA TEJA VELPURI			
49 6	16004 0160	CHILAKALA VISHNU			
49 7	16004 0565	MOPARTHI AKHIL DEEPAK	Dr.M.Siva Ganga Prasad	180	Frequency reconfigurable antenna for UWB applications
49 8	16004 0178	DANDURI PAVANI			
49 9	16004 0864	TADIKONDA DURGA LAKSHMI NAGA PRATHYUSHA			
50 0	16004 0020	ALURU VENKATA SAI KRISHNA	Dr.N.Phalguni Singh	181	Comparitive analysis of distributed arithmatic based approximate sum of products design
50 1	16004 0478	MADALA BRAMHENDRA			
50 2	16004 0744	PUTTA LAKSHMI GANESH			
50 3	16004 1056	AKULA JASWANTH SAI SURYA SANDEEP	Dr.Sourabh Upadhyay	182	Design of magnetude comparator using adiabitic logic in a ECRL
50 4	16004 0183	DASARI RAVEENDRA BABU			
50 5	16004 0047	ARIKATHOTA SAI UMESH			
50 6	16004 0054	AVUTHU DINESH REDDY	Dr K.Hari Kishore	183	Hybrid full adder based architecture of swing dependant XOR and XNOR gates
50 7	16004 0877	TEJASWI YARRAMASU			
50 8	16004 1043	MULLAPUDI VENKATA NARENDRA			
50 9	16004 0077	BAYANA MANOJ VENKAT	Dr.K.Hari Kishore	184	Power analysis LUT using APC and OMS algorithms
51 0	16004 0823	SHAIK MOHAMMED REYAAN			
51 1	16004 1023	GUNDA YOGA VENKATA SIVA SHANKAR			
51 2	16004 0120	BOPPUDI SUSMITHA	Dr N.Siddaiah	185	A novel MEMs based alchohol gas sensor using nano particles
51 3	16004 0582	MUPPASANI MAHESH BABU			
51 4	16004 1008	KANKANAMPATI MANISHA	Dr.Sourabh Upadhyay	186	Sequential adibatec logics for low power applications
51	16004	CHANCHALA SESHASAYANA			

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517	16004 0457	KURALLA GNANA NAGA SIVA SANKAR	Dr.Mr B.Murali Krishna	187	Reconfigurable light weight algorithms for data security applications
518	16004 0223	DUVVURI SAI SURYA MOUNIKA			
519	16004 0167	CHINNALA SWETHA			
520	16004 0572	MUKKAMALLA MANEESHA	Dr G.R.K.Prasad	188	Desgin of Piezo electric MEMs resonator
521	16004 0604	NALLABOTHULA RAMYA			
522	16004 0790	SAI TEJASWI VARIKUTI			
523	16004 0235	GADDAM SAI KIRAN	Mr G.V.Ganesh	189	Design and analysis of different parameters of shunt based RF MEMS switch
524	16004 0316	JANAMALA HEMANTH			
525	16004 0455	KUNDURU SAI PUNEETH REDDY			
526	16004 0355	KANCHI V S SUBBA RAO	Dr.Sourabh Upadhyay	190	Design of arthimatic core using scan flip flop
527	16004 0294	GURRAM HARSHITH			
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529	16004 0397	KAVYA CHOWDARY VELUVOLU	Mr. M. Aditya	191	Noise reduction in MOSFET using multigate technology
530	16004 0923	VALIBOYINA NAVEEN SAI			
531	16004 0434	KOPANATI SAI NISHITHA			
532	16004 0661	PALLEPOGU DIVYA	Dr. S. Rooban	192	A fault tolerant hancarlson adder using TMR technique
533	16004 0738	PULLETIKURTHI KANAKA VENKATA SAI PREETHI			
534	16004 0220	DUNGA BHUVANA STAYA			
535	16004 0261	GOGINENI RAHUL	Dr.N.Phalguni Singh	193	A high speed precision controllable approximate 16-bit multiplier
536	16004 0532	MARRIPALAPU ATINDRA CHANDRA SEKHAR			
537	16004 0699	PEDAPATI JAYA VENKATA SAI KALYAN			
538	16004 0881	THADIKONDA HARSHA VARDHAN	Dr.P.Pardhasaradhi	194	Design of comparators using adders
539	16004 0612	NARAMSETTI HARI KIRAN			
540	16004 0649	PACHIPULUSU VENKATA SAI JAYA SATHWICK			
541	16004 0755	RAMISETTY KARTHIK	Dr.China Satyanarayana	195	Prediction and analysis of future indian climate using down scaling methods

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543	160040622	NATUKULA LIKITH CHANDRA SIDDA SAIKUMAR			
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547	160040330	K ASHOK SHIVA REDDY	Mr.Muzammil Parvez M	197	Securing IOT data using crypt analysis
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550	160040135	CHALLA SRINIVASA REDDY	Mr. K.Gopi Ram	198	Skin cancer detection using ABCD rule
551	160040591	MYLA GANESH			
552	160040763	RAYALA HARSHITH KUMAR			
553	160040615	NARAYANASETTI CHANDRA MOULI	Ali Baig Mohammad	199	Neural style transform
554	160040719	POLURI SAINATH			
555	160041047	KAMMILI CHIRANJEEVI BABU			
556	160040339	KAKARLA PAVAN KUMAR	Md. Buran Basha	200	Number plate detection of motor cyclist who are not wearing helmet
557	160040501	MAHAVADI VENKATA SAI DURGA ANUDEEP			
558	160041027	SINGAMSETTY RAVI TEJA			
559	160040375	KARETI RAHUL KRISHNA	k.Rajesh Babu	201	Effective detection of brain tumor on MRI image using optimization based advanced clusterring techniques
560	160040542	MEDISETTI GOWTAM SAMHITH			
561	160040653	PAGOLU JAMEEMA PUSHPITHA			
562	160041034	DONDAPATI APPAJIBABU	Dr.MVD Prasad	202	Intelligent crop recommendation system for farmers based on soil nature, rain fall, previous market demand using machine learning techniques
563	160041049	NAGULAKONDA VENKATA SAI KRISHNA			
564	160040717	POLAVARAPU SATYA DURGA LALITHA SARMA			
565	160040958	VENNA VENKAT KUMAR	Ms.A.Sree Madhuri	203	Estimating the effect of rain attenuation on free space optical links
566	160040419	KOMMANA NAGA SAI LAXMI MALATHI			
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56	16004	MUMMAREDDY YOGENDRA SAI	Ms.Mona	204	Cognitive traffic violation detection using artificial

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573	16004 0800	SATTENAPALLI NEELAKANTHA SAIRAM			
574	16004 0410	KOKATAM VINAY KUMAR REDDY			
575	16004 0802	SATTU SARVANI	Mr.K.Nageswara Rao	206	How energy efficient in wireless communication system become
576	16004 1045	GORU PAVAN KUMAR			
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579	16004 1019	KARIGADALA RUDREGOWDA BHARAT			
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583	16004 0462	KURRE LAHARI			
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586	16004 0642	NUNNA SAI SOUMITH			
587	16004 1004	CHODAVARAPU SIVA BHARGAVI	Mr.J.Bennilo Fernandes	210	Emotion speech recognition using SVM and fuzzy logic
588	16004 0357	KANDALAM KARTHEEK			
589	16004 0287	GUNISETTI SATYA SURYA SAI SANDEEP			
590	16004 0286	GUNDU SAI KUMAR			
591	16004 1005	DARAPUREDDY SESHA SAI KRISHNA	Mr.Ajay Nagendra	211	Design 16X16 vedic multiplier using 10T full adde
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596	160040496	MADIRA BINDU MEGHANA			
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599	160040258	GHANTA RAJASEKHAR	Mr.Agilesh Saravanan	213	Design and analysis of full adder by using different kinds of adiabatic logics
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601	160040952	VEMPATI RAMYA REDDY			
602	160041009	KODI SINDHUJA	Mr.I.Veera Raghava Rao	214	A low power 2-bit magnitude comparator using adiabatic logic
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610	160040068	BANOORI VINEETH REDDY			
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52 2	16004 0022	AMBALAKARRA SAI DURGA SRAVANI	Mr.Aravind Kilaru	220	Stratiform and convective rainfall vertical structure observations fromm MRR
52 3	16004 1040	MANDAVA KOKILA LAKSHMI			
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52 9	16004 0561	MOHAMMED KALEEMUL ASHWAQ	Dr.S.Koteswara Rao	222	Movie Recommendation System using Machine Learning
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555	160040525	MANNEPALLLI VAMSHI			
556	160040246	GANGALA BABJI			
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657 6	16004 0939	VEERA LOHIT K	Dr.P.Rakesh	237	Causative attacks and its counter measures
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658 3	16004 0791	SAKHAMURI BHARATH CHANDRA	Dr.N.Bala Chakravarthi	239	Land mark localization for cephlograms using convolutional neural network
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658 8	16004 0764	RAYANA ROHIT			
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659 2	16004 0674	PARCHURI KRISHNA TEJA			
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659 5	16004 0063	BANDI SRUTHI ANMISHA			
659 6	16004 0530	MARRAPU SIRI CHANDANA	Mr.Durga Prasad Tripati	243	Home application using CISCO packet tracer
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714	160040625	NEDURI SWATHI KIRAN	Dr.Ch.Santhosh	248	Binary to gray convertor using QCA
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716	160040901	TUMMALA VENKATA KIRAN KUMAR			
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723	160040276	GORRIPATI SIVARAM			
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741	160040239	GADE SAI SRI RAM REDDY	Mr.LSP Sairam Nadipalli	256	Enhancement of 32 bit adder using different configuration adders
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754	160040219	DULAM VENKATA SIVA PRASAD			
755	160040389	KATIKALA NIKHEL SATHVIK	Mr.B.Suresh (P.School)	260	Fuzzy decision tree based subsurface anomoly detection using thermal wave imaging
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761	160040380	KARUMURI DINESH REDDY			
762	160040019	ALURI SAI PRAGNA	Mr.M.Venu Gopal Rao	262	Design and simulation of MEMs based respiratory sensor interfacing with IOT
763	160040577	MUNNANGI GEETHA SWAROOPA			
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783	160040792	SAKHAMURU BANDHAVI SAI SRI			
784	160040440	KOTARI CHANDRA SEKHARAM			
785	160040497	MADIRAJU ADARSH SHARMA	Dr. M. Venkata Narayana	269	Configuration of IPV6 firewall for different protocals
786	160040679	PASAM ADITYA SAI RAM			
787	160040959	VENNAPUSA ABHILASH REDDY			
788	160040614	NARAYANA PAVAN SRINIVAS	Dr.MVD Prasad	270	Leaf disease detection using matlab
789	160040627	NEMELA PAVAN SATYA NAGA SAI			
790	160041035	JADDU JAYASAI KISHORE			
791	160040034	ANNAVARAPU VAMSI KRISHNA	Dr. E. Kiran Kumar	271	Prediction of brain tumors using Neural network
792	160040055	AVUTHU PURANDAR REDDY			
793	160040314	JAMPANI NAGASAIKUMAR			
794	160040514	MANDADAPU PAVAN KUMAR	Dr. P V V Kishore	272	Disaster classification using SVM and genetic algorithm
795	160040568	MOVVA MANORAMA			
796	160040492	MADDULURI SRI HARSHA			
797	160040385	KATAKAM NAGA KAMALAKAR REDDY	Dr.D.Bhavana	273	Lane detection for driver assistance system using open CV
798	160040484	MADARABOYINA SAITEJA			
799	160040948	VELLATURI VENKATA PRAVEEN			
800	160040036	APPALA VENKATA AJAY KUMAR	Dr. P. Satyanarayana	274	Paralysis patient health care using IOT
801	160040310	JAHNAVI YALLA			
802	160040225	EANUMULA ESWAR SAI YASHWANTH			
803	160040103	BODIREDDY SATYA DURGA SAI KUMAR	Dr. I. Govardhini	275	A novel method to detect obstructive sleep apnoea using ECG signas based on machine learning methods
804	160040277	GORUGANTU NAGA SURYA PRAKASH			
805	160040037	APPIKATLA KEERTHI SRI			
806	160040252	GARIMELLA CHANDRAKANTH	Dr. G V Subbarao	276	Prediction of chronic kidney disease using machine learning techniques

307	160040459	KURNALA KASMIRA			
308	160040485	MADASI SAHITHI			
309	160040593	N C SRI HARSHA	Mr. K. Rajesh Babu	277	A low cost device for GPS inospheric scintellation monitoring for space weather studies
310	160040973	Y GIRISH VENKATA SAI ANUDEEP			
311	160040449	KUDARAVALLI VIKASH			
312	160040009	ALAHARI POOJITHA DHANVI	Dr.C.S.Preetham Reddy	278	Analysis of emotions in speech signal using MATLAB
313	160040399	KHALID MUHAMMAD			
314	160040227	EDARA YASASWINI			
315	160040089	BHEMAVARAPU GOPINATH REDDY	Mr.M.Venu Gopal Rao	279	Practical implementation of user centric machine learning frame work for cyber security operations with visualizations
316	160040538	MEDA MOHAN VAMSI			
317	160040603	NAKKA SRI KRISHNA CHAITANYA			
318	160040595	NADENDLA BHARGAVA SAI			
319	160040008	AKUNURI RAHUL RATNAM	Dr.Ch.Santhosh	280	Enhanced end-to end encription using custom cifering
320	160040013	ALLA GANESH			
321	160040426	KONDA SAI CHARAN REDDY			
322	160040869	TAMMA MANEESH REDDY			
323	160040226	EDARA VENKATA SAI PAVAN	Dr.D.Bhavana	281	Security assignment to WAN using CISCO tracer
324	160040701	PEDDI SAI ANIL			
325	160040062	BANDI SRIKEERTHANA REDDY			
326	160040289	GUNNAM PAVANI LAKSHMI	Dr.G.Siva Vara Prasad	281	Performance analysis of capacitive PH sensor for biomedical applications
327	160040078	BAYIREDDY RAKESH			
328	160040392	KATTA BHARGAVARAM			
329	160040402	KODALI BHUVANA VENKATA AAKARSH			

Koneru Lakshmaiah Education Foundation
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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event **:** 07-01-2020

Name of the Event **:** TI WorkShop

Venue **:** R104(Lab)

No. of Participants **:** Students: 47, Staff: 04

A Report on TI WORKSHOP on 07-01-2020

Objective:

The objective of TI WORKSHOP was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering.

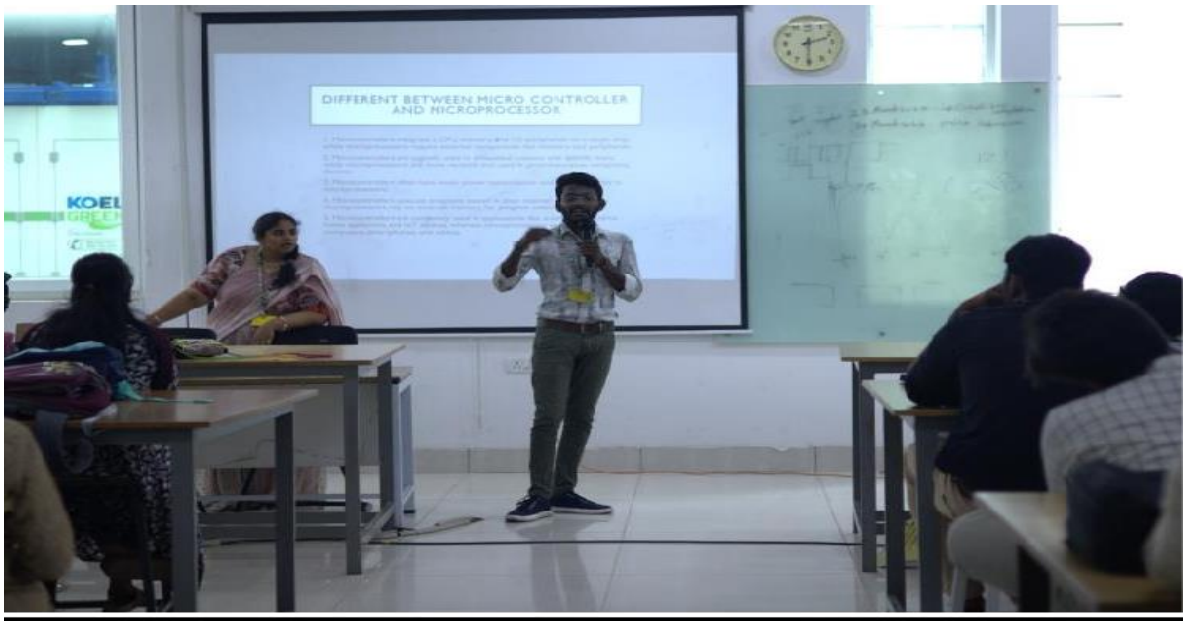
Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted "TI WORKSHOP" program on 20-04-2024 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of N.Durga Indira, Y.Usha Devi, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of TI WORKSHOP encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.

In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Event Structure:

TI WORKSHOP comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

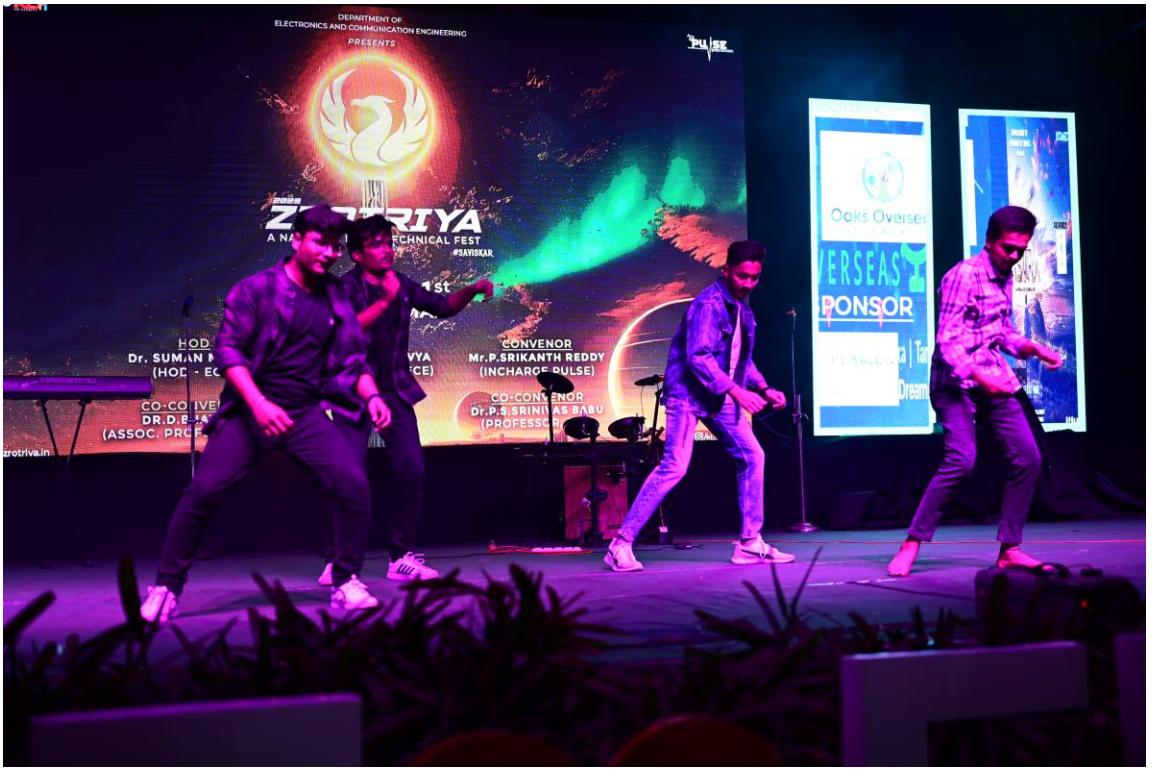
TI WORKSHOP achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, TI WORKSHOP fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

TI WORKSHOP, as part of **"Zrotriya"** played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like TI WORKSHOP are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.

Culturals



Head of the Department